

DIGITAL BROADCAST RECEIVER

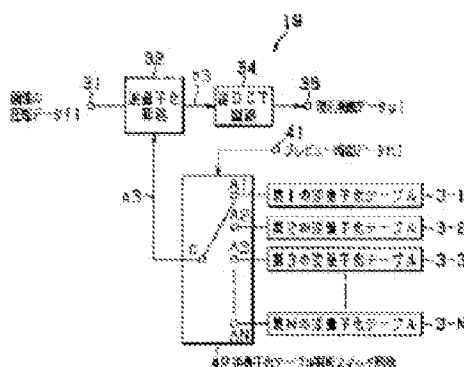
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Applicant: TOKYO SHIBAURA ELECTRIC CO
Classification:
- **International:** **H04N7/16; H04N7/16;** (IPC1-7): H04N7/16
- **European:**
Application number: JP19970329084 19971128
Priority number(s): JP19970329084 19971128

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Abstract of JP11164281

PROBLEM TO BE SOLVED: To enhance utility promotion effect of a preview service.

SOLUTION: An inverse quantization circuit 32 multiplies a quantization step a3 obtained from each inverse quantization table connecting to an inverse quantization table selector switch circuit 42 with each pixel of each square area in compressed data f1, to obtain a discrete cosine transform (DCT) coefficient b3 and gives it to an inverse DCT circuit 34. The inverse DCT circuit 34 applies inverse DCT arithmetic operation to the DCT coefficient b3, generates decoded image data g1 and leads the data g1 to an output terminal 35. The inverse quantization table selector switch circuit 42 selects a inverse quantization table in use in the order of a 1st inverse quantization table 3-1, a 2nd inverse quantization table 3-2, a 3rd inverse quantization table 3-3,..., an N-th inverse quantization table 3-N to limit a video band (resolution) of a decoded video image by the decoded image data g1 stepwise.



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